Appl. No. 10/687,771 Amdt. dated August 27, 2007 Reply to Office action of August 16, 2007

## **Amendments to the Claims**

This listing of claims will replace all prior versions, all listings, of claims in the application:

## **Listing of Claims:**

Claim, 1 (Previously Presented): A transceiver of a communication system, comprising:

- a front-end receiver for generating a first signal with a pre-cursor component and a post-cursor component according to a receiving signal, wherein the front-end receiver further includes an inverse partial response (IPR) filter to compensate an ISI introduced by a partial response filter in a transmitter part of a remote transceiver and an analog-to-digital (A/D) converter to receive an output signal of the IPR filter and convert the output signal to the first signal with a digital format;
- a noise canceller coupled to the front-end receiver for generating a second signal through eliminating the noise of the first signal;
- a Feed-Forward Equalizer (FFE) coupled to the noise canceller for generating a third signal through eliminating the pre-cursor component in the second signal according to a transfer function including a plurality of adjustable constants, wherein the adjustable constants includes a main-tap and the value of the main-tap is predetermined; and
- a decoder coupled to the FFE for decoding the third signal and eliminating the post-cursor component in the third signal.
- Claim 2 (Original): The transceiver as claimed in claim 1, wherein the front-end receiver further includes a sample-and-hold circuit to sample and hold the receiving signal.
- Claim 3 (Previously Presented): The transceiver as claimed in claim 2, wherein the transceiver further includes a timing recovery coupled to the decoder for controlling the sample-and-hold circuit according to an output signal of the decoder.

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Claims 4-7 (Cancelled)

A:
Claim & (Previously Presented): The transceiver as claimed in claim 1, wherein the IPR
filter is an infinite impulse response filter.